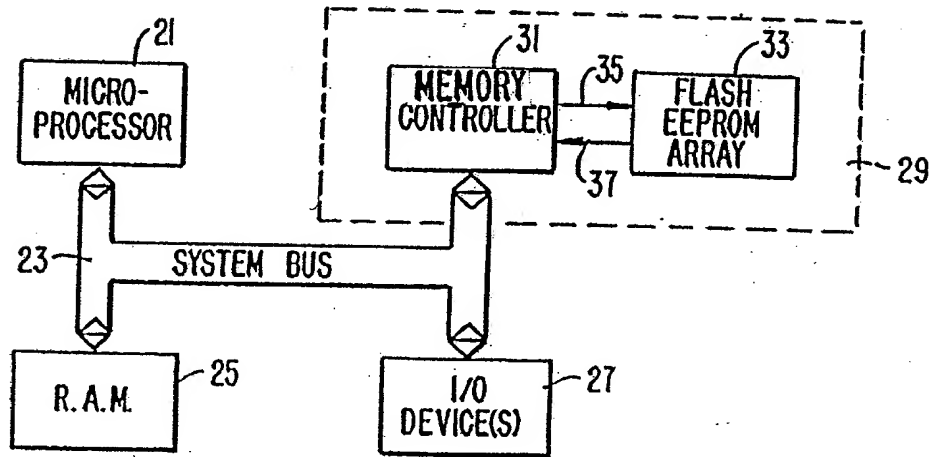
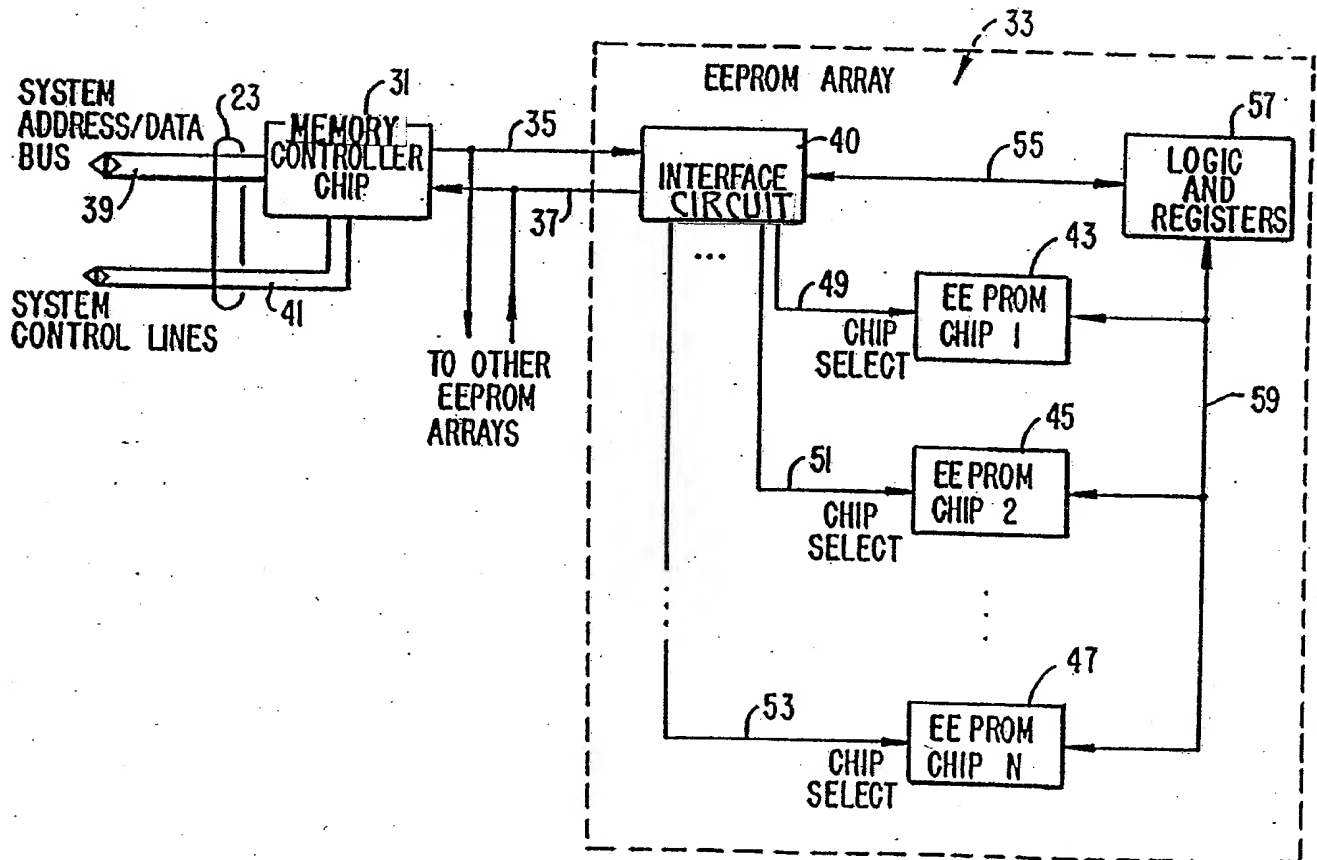


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**FIG. 1A**



**FIG. 1B**

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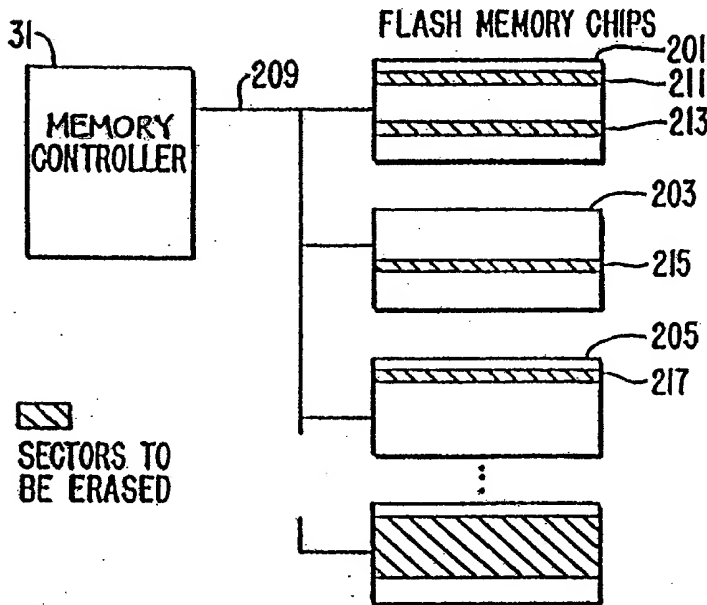


FIG. 2

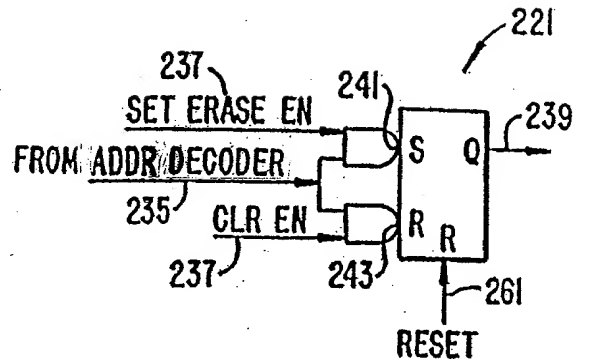


FIG. 3B

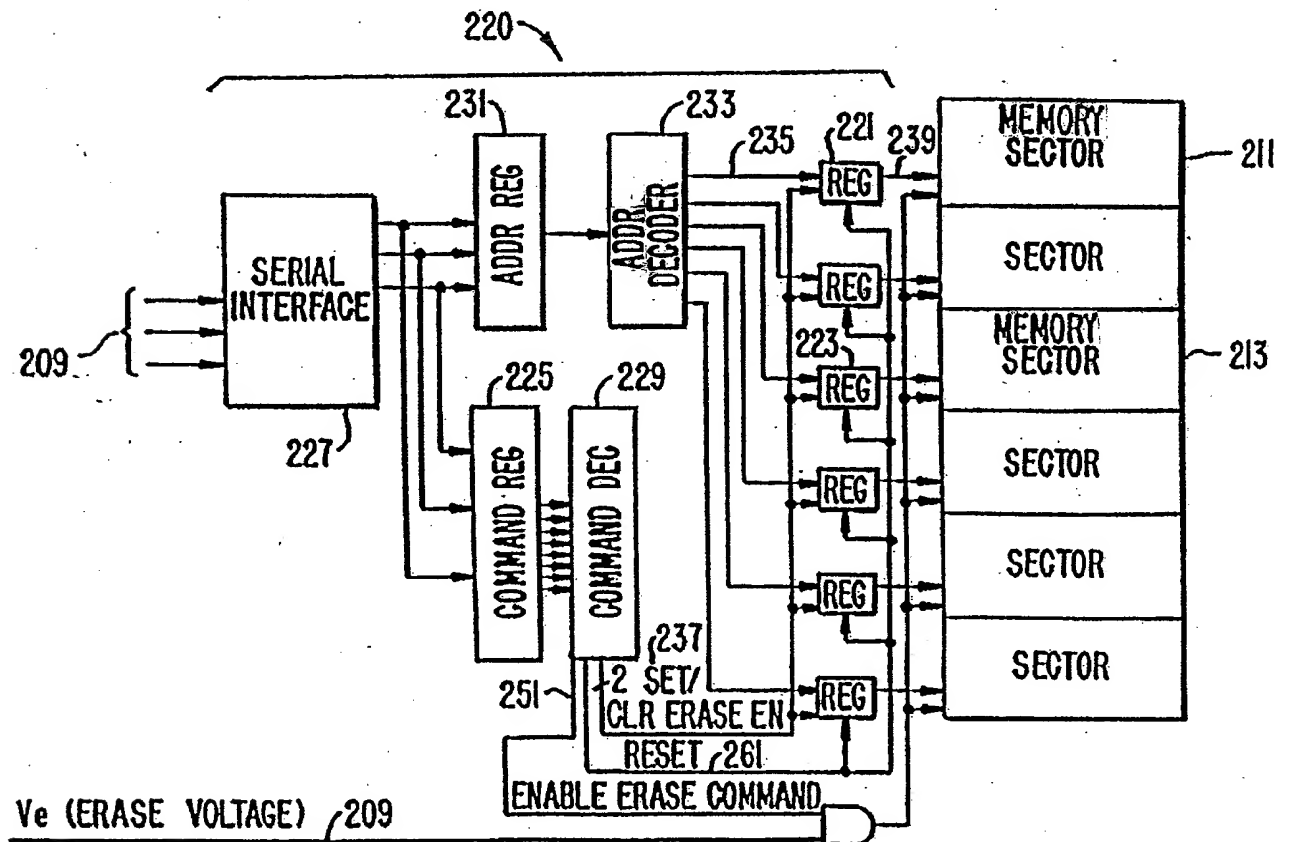


FIG. 3A

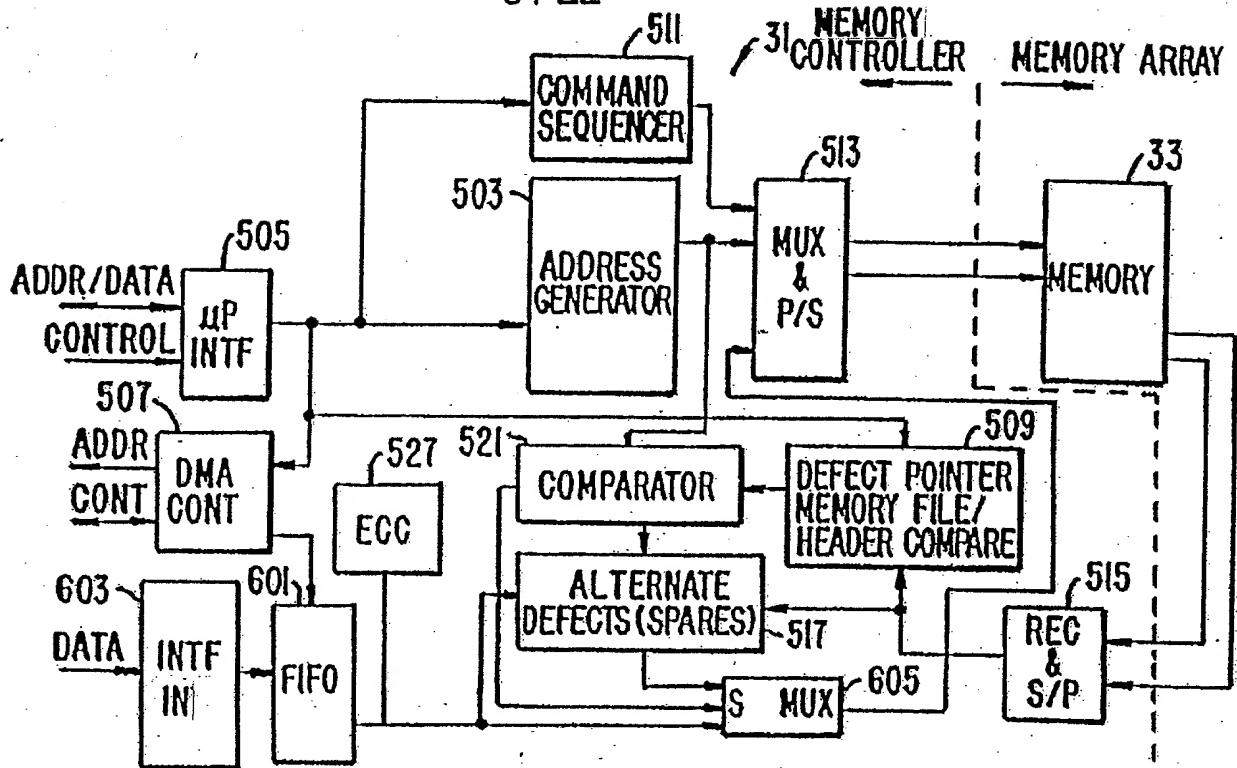
Diagram illustrating a Sector Partition structure. The partition is divided into several fields:

- 401**: Unlabeled top field.
- 403**: DATA field.
- 407**: ALTERNATIVE DEFECTS DATA field.
- 409**: DEFECT MAP field.
- 411**: HEADER field.
- 413**: ECC & OTHERS field.
- 405**: A bracket indicating the group of fields 407, 409, 411, and 413.

The entire structure is labeled **SECTOR PARTITION** at the bottom.



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WRITE DATA PATH CONTROL

FIG. 7

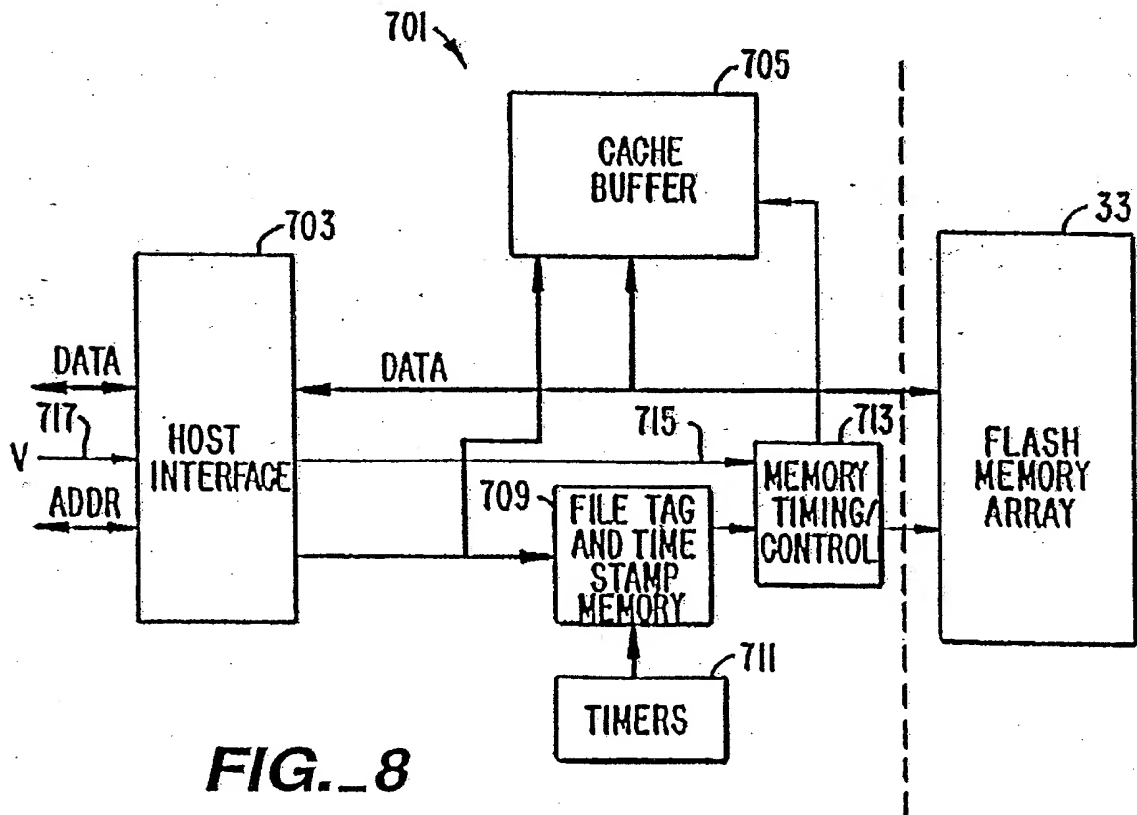
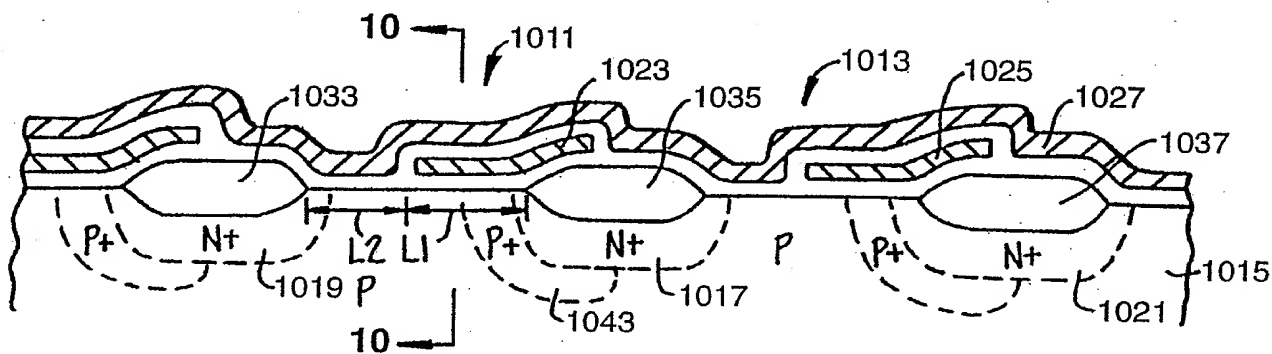
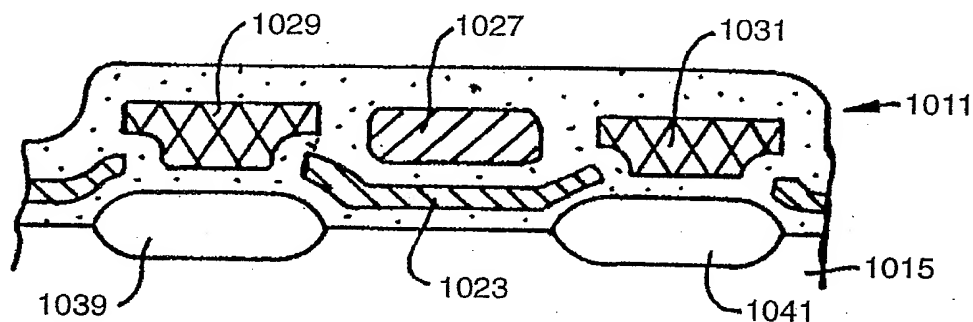


FIG. 8

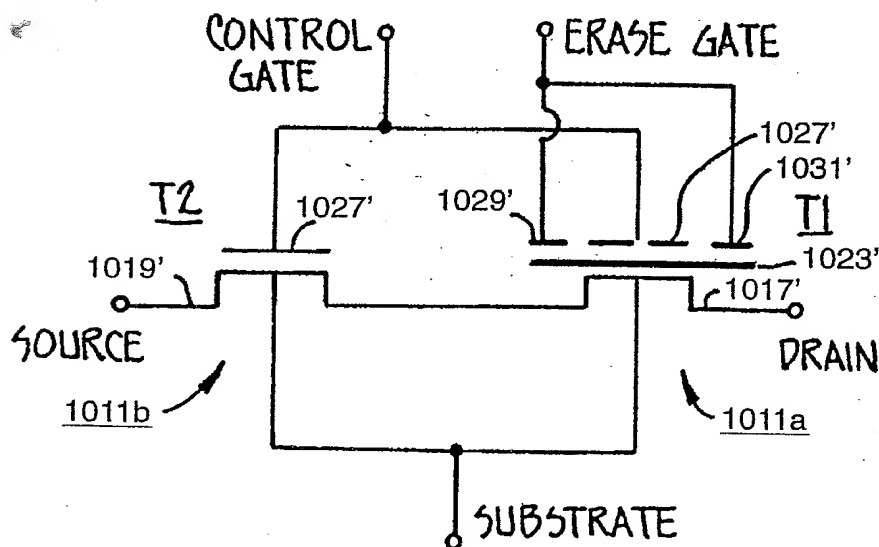
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**FIG. 9**



**FIG. 10**



**FIG. 11**

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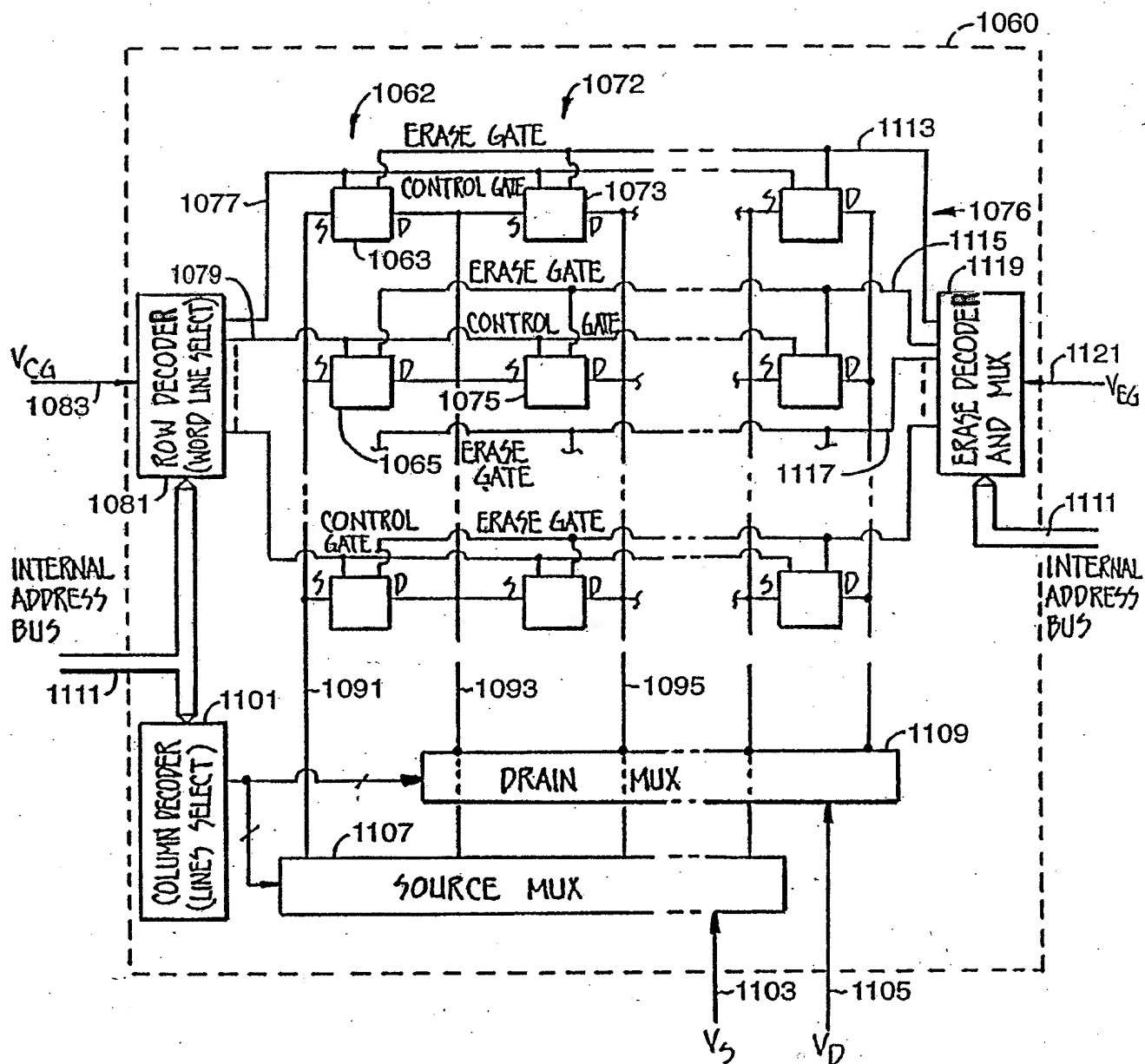


FIG. 12

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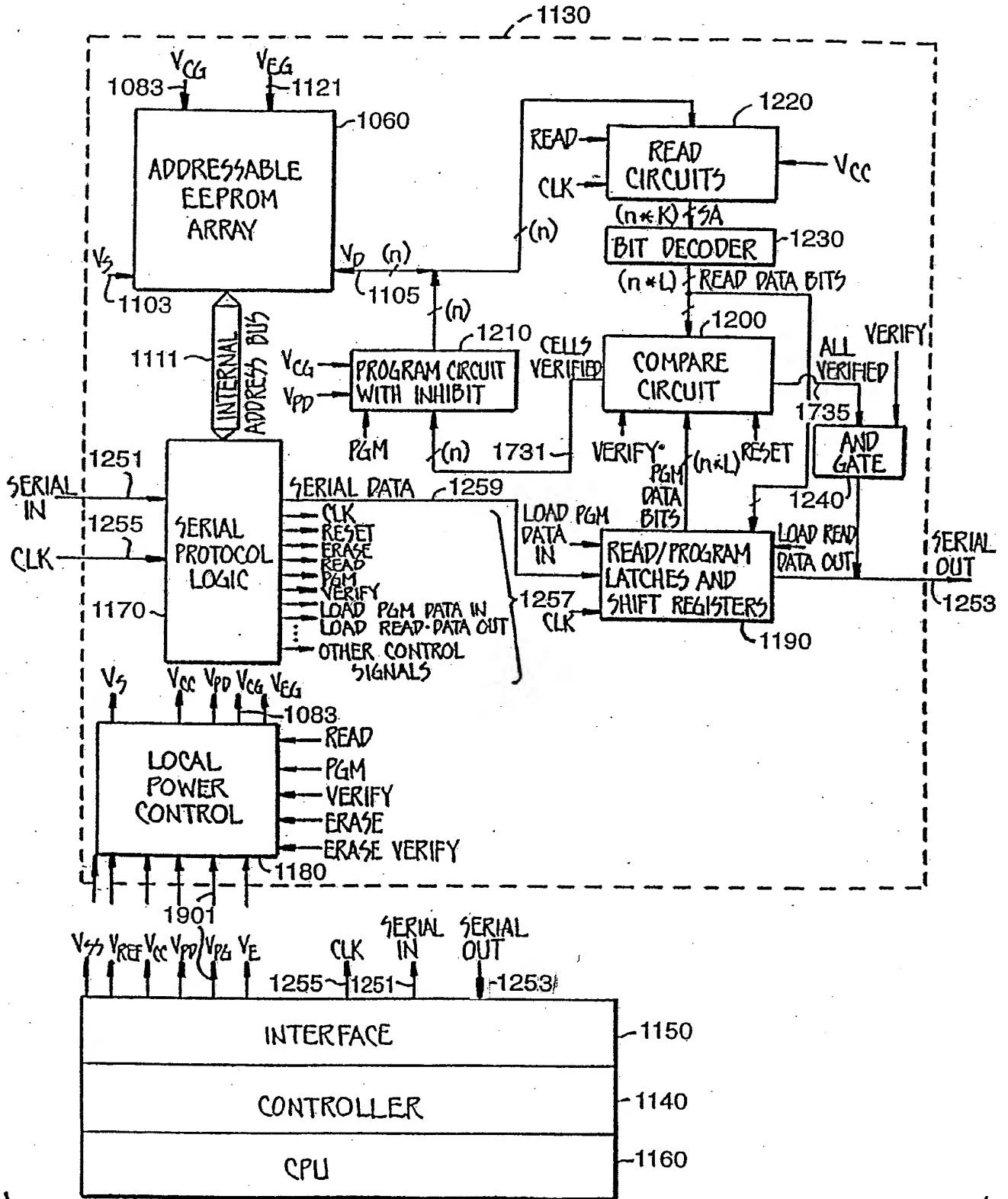


FIG. 13

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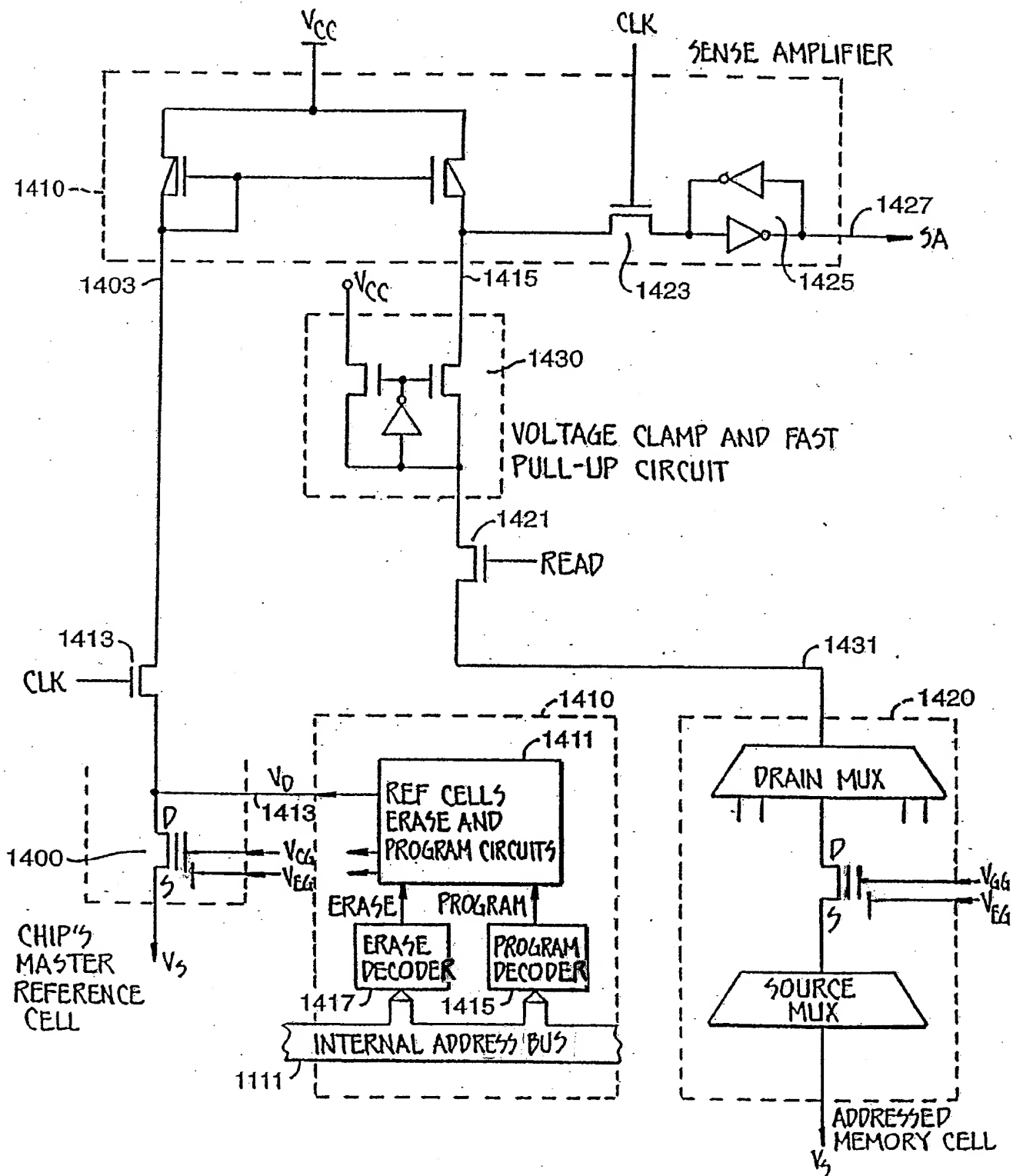


FIG. 17A



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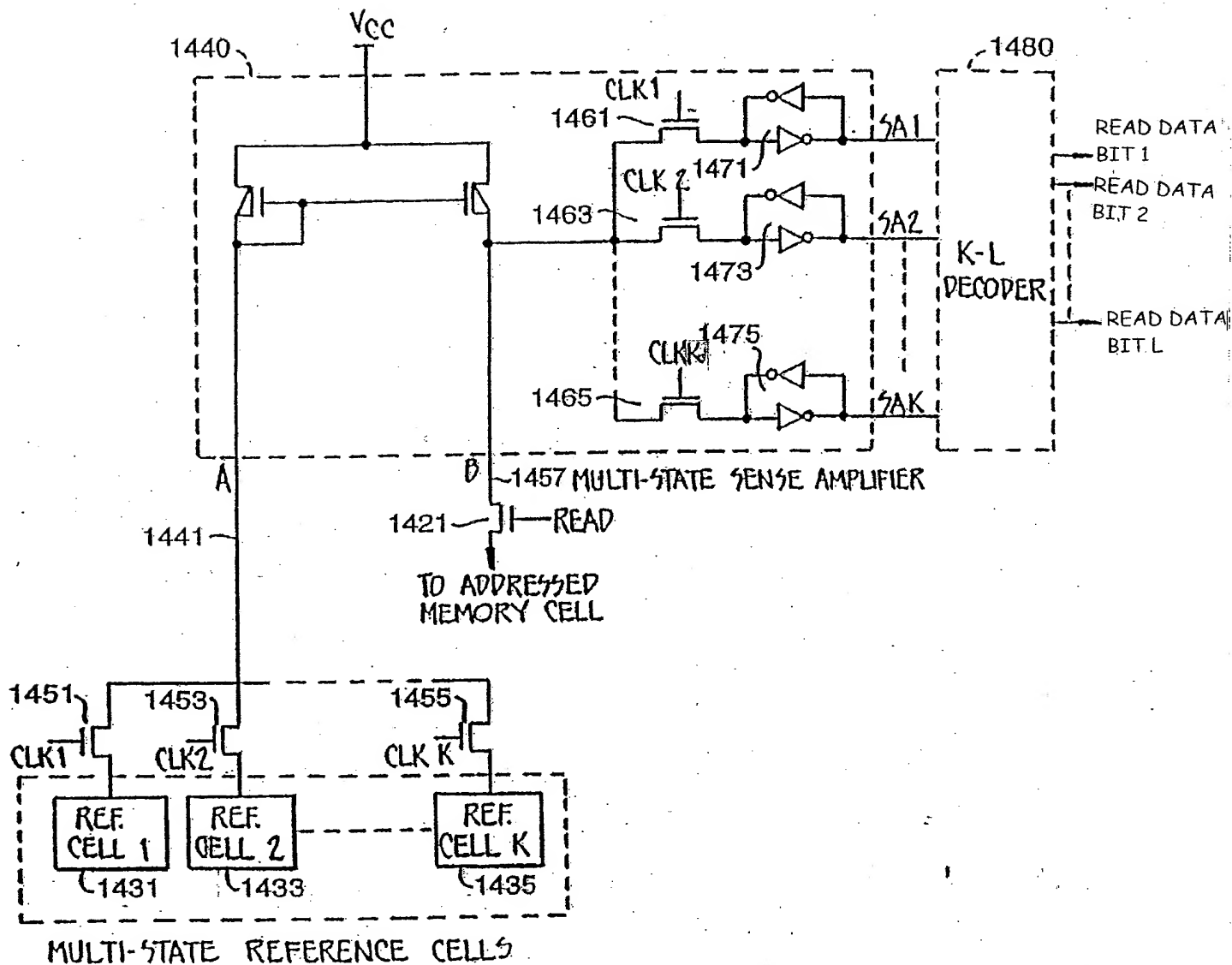
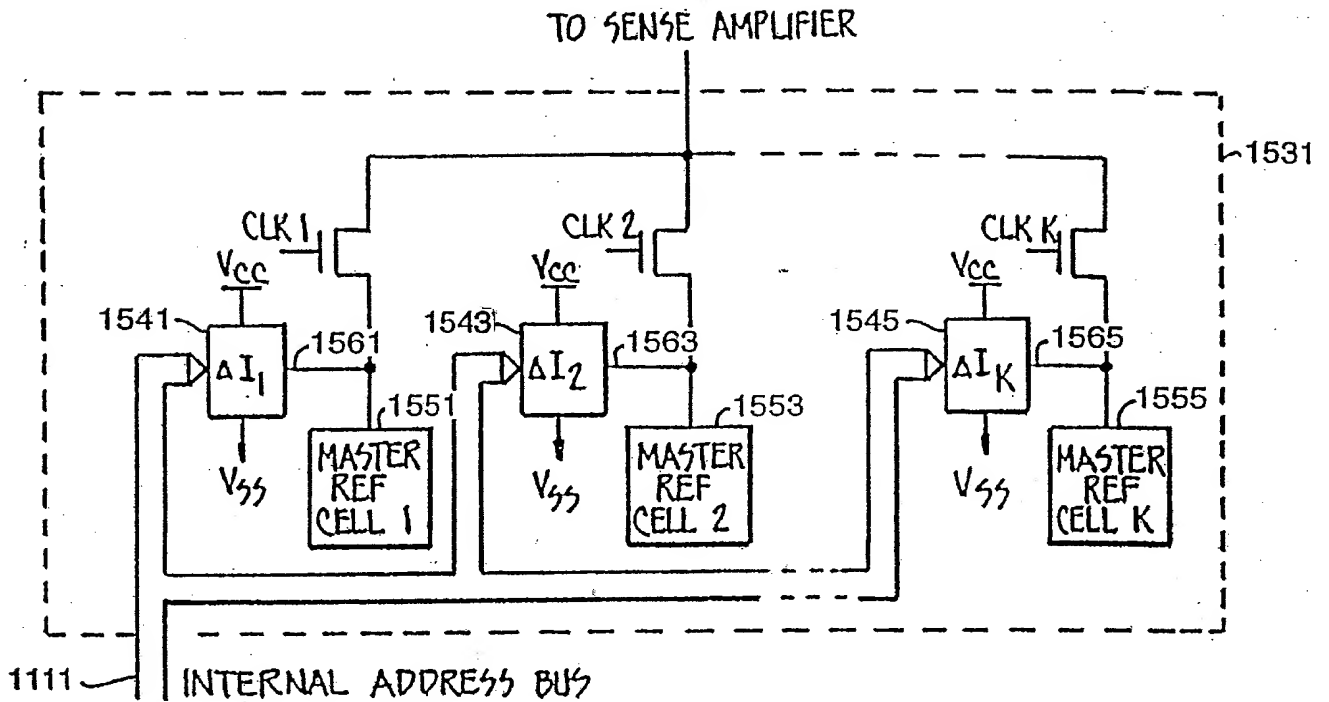
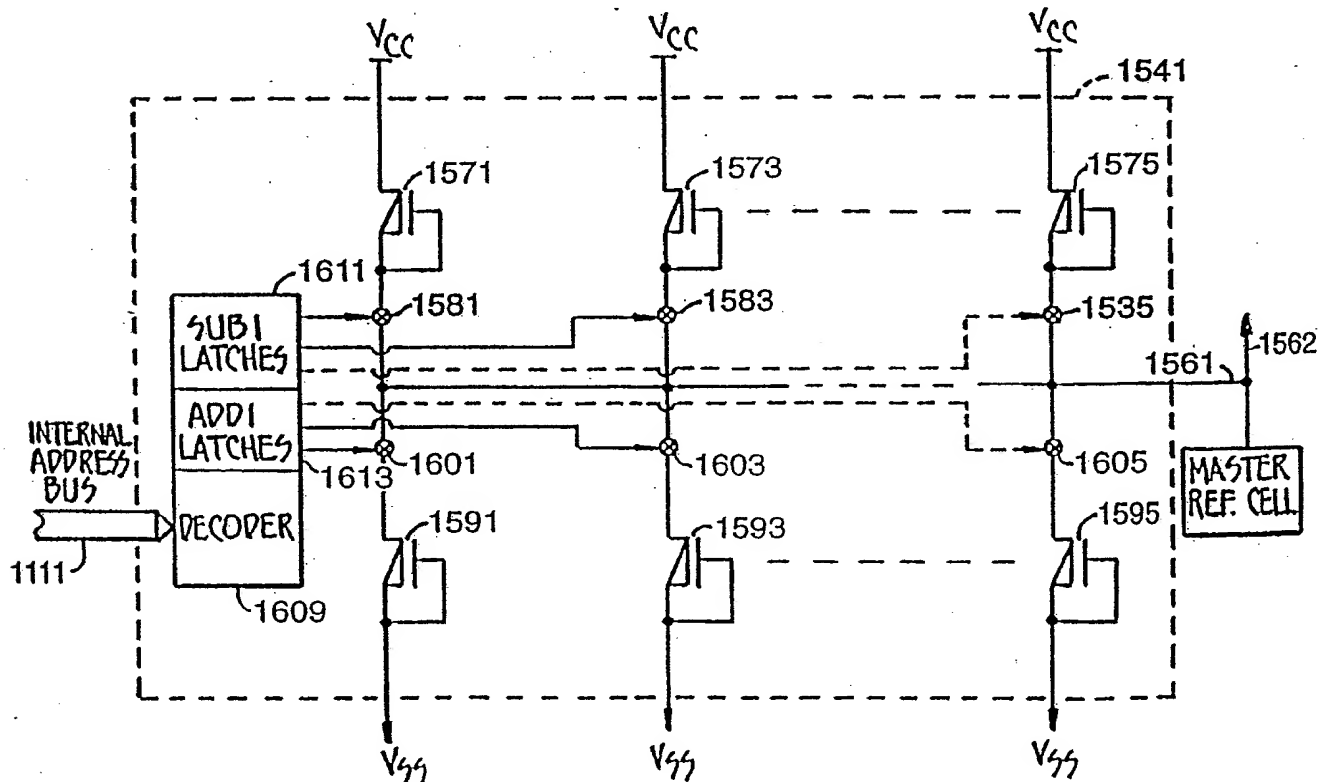


FIG. 17B

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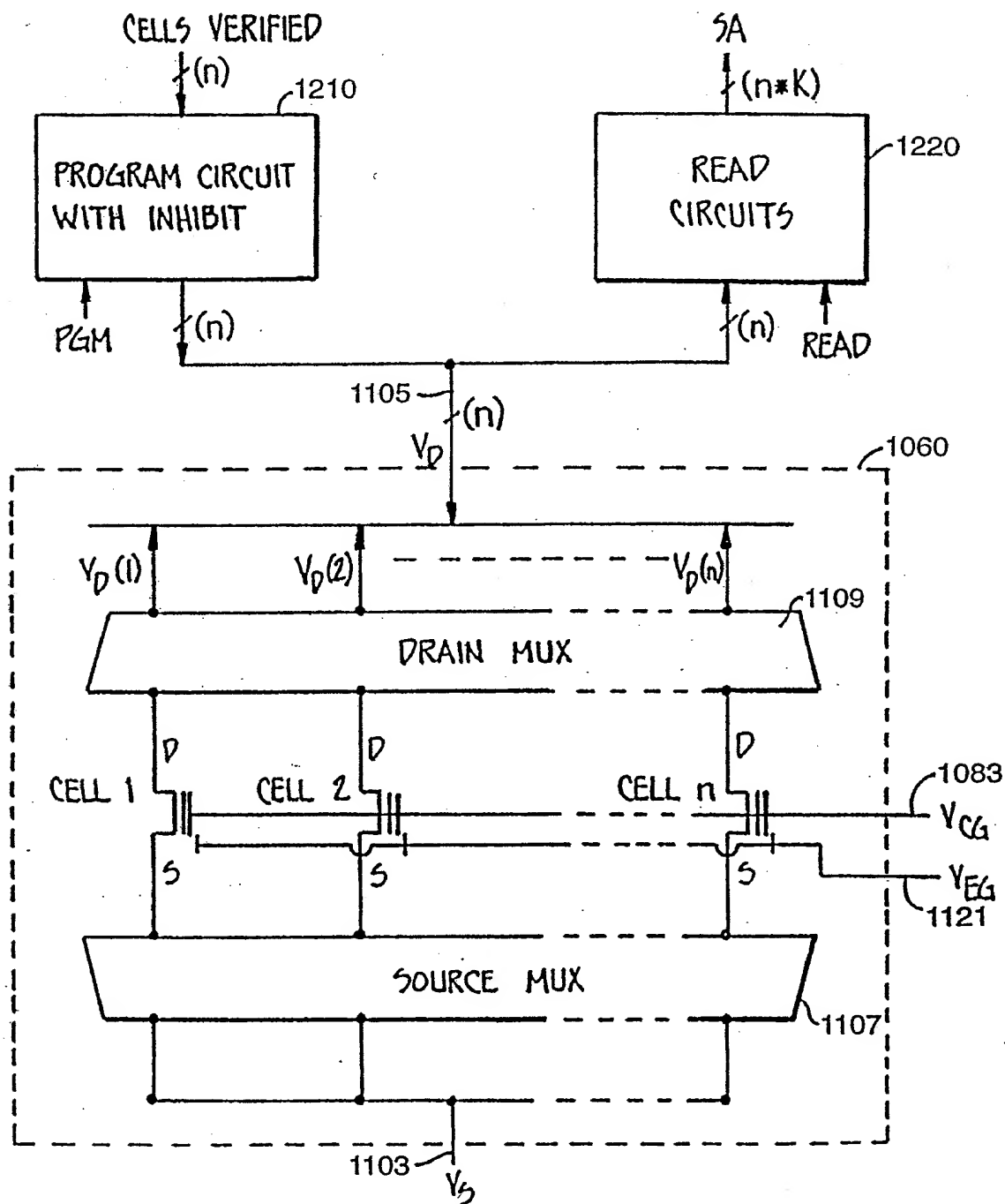


**FIG. 21B**



**FIG. 21C**

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READ/PROGRAM DATA PATHS FOR  $n$  CELLS IN PARALLEL

**FIG. 22**